| \$ | DDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDD | ************************************** | |
|--|---|--|--|
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| \$\$\$ \$\$\$ \$\$\$\$\$\$\$\$\$\$\$ | DDD DDD DDD DDD DDD DDD | AAA AAA AAA AAA | |
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| SSSSSSSSSSS SSSSSSSSSSS SSSSSSSSSSSS | DDDDDDDDDDDD DDDDDDDDDDDD DDDDDDDDDDDD | AAA AAA AAA AAA | |

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LIB

VALIDATE Table of contents - Structure Validation module 16-SEP-1984 01:48:32 VAX/VMS Macro V04-00 Page 0 (1) (1) (2) 53 81 122 DECLARATIONS
VAL_SET_MAX - Set maximum number of links to traverse
VALIDATE_QUEUE - Validate queue structure

LIB VO4 K 8

LIB VO4

```
.TITLE VALIDATE - Structure Validation module .IDENT 'VO4-000'
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ÖÖÖÖ
0000
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0000
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0000
             : FACILITY:
0000
          31 ;
0000
0000
                       SDA
0000
0000
             : ABSTRACT:
0000
          35
          36
37
0000
                       This module contains code which verifies the consistency of
0000
                       certain VMS internal structues.
0000
          39
0000
               ENVIRONMENT:
0000
         41 :
0000
                       VMS user mode.
0000
0000
0000
0000
             ; AUTHOR: Jake VanNoy, CREATION DATE: 21-Jan-1983
0000
          47
0000
               MODIFIED BY:
0000
         49:
                       V03-000 JLV0226
0000
                                                      Jake VanNoy
                                                                                   21-JAN-1983
0000
          50
                                 Initial coding.
         51 :**
0000
0000
0000
                        .SBTTL DECLARATIONS
0000
             : INCLUDE FILES:
0000
0000
```

```
- Structure Validation module DECLARATIONS
                                                           16-SEP-1984 01:48:32 VAX/VMS Macro V04-00 5-SEP-1984 03:34:48 ESDA.SRCJVALIDATE.MAR;1
                       58
59
                                     SOPTDEF
                                     STPADEF
                       60
                       61 62 63
                             MACROS:
                       64
                           : EQUATED SYMBOLS:
0000000
                           NEWFL = 0
00000004
                           NEWBL = 4
0000008
                           HEADFL = 8
0000000
                           HEADBL = 12
                           LOCAL_STORAGE = 16
00000010
                       75
76
77
                             OWN STORAGE:
00000190
                           VALIDATE_MAX:
                                                          400
                                                .LONG
                                     .SBTTL VAL_SET_MAX - Set maximum number of links to traverse
                       82
83
                          ;++
             0004
             0004
                             FUNCTIONAL DESCRIPTION:
             0004
                                     Tparse action routine which sets value of VALIDATE_MAX.
                                     *** Note that the VALIDATE QUEUE code does not use this *** as input. It has not been proven that there are 'infinite'
             0004
                       89
             0004
                                     *** queues which the algorithm does not detect.
             0004
             0004
                             CALLING SEQUENCE:
             0004
                                     NONE
             0004
             0004
                             INPUT PARAMETERS:
                       96
97
             0004
                                     NONE
             0004
                       98
99
             0004
                             IMPLICIT INPUTS:
                                     NONE
             0004
             0004
                      100
            0004
0004
0004
0004
                      101
102
103
                             OUTPUT PARAMETERS:
                                     NONE
                      104
                             IMPLICIT OUTPUTS:
                      105
                                     NONE
             0004
0004
0004
0004
0004
                      106
                             COMPLETION CODES:
                      108
109
110
                                     NONE
                             SIDE EFFECTS:
                      111
112
113
114
                                     NONE
```

L 8

LIBS

Page

(1)

VALIDATE VO4-000

VO4

```
.SBTTL VALIDATE_QUEUE - Validate queue structure
                                          ;++
                                            FUNCTIONAL DESCRIPTION:
                                                    The algorithm used to validate a doubly linked list queue
                                                   starts by copying the head (or starting point specified) into a known location. It then proceeds by following the forward link checking that the backward link points to where
                                                    the last forward link was.
                                            CALLING SEQUENCE:
                                                    CALLS from tparse.
                            000F
                            ÖÖÖF
                                             INPUT PARAMETERS:
                            000F
                                                    TPA$L_NUMBER(AP) - address to start search from
                            000F
                                     139
                            ÖÖÖF
                                            IMPLICIT INPUTS:
                            ÖÖÖF
                            ÖÖÖF
                                                    OPTIONS - can specify SELF RELATIVE QUEUE *** code not writeen for this as yet.
                            000F
                            000F
                            ÖÖÖF
                                            OUTPUT PARAMETERS:
                                     145
                            ÖÖÖF
                                                    NONE
                            000F
                                     147
                            000F
                                            IMPLICIT OUTPUTS:
                            OUOF
                                     149
                                                    NONE
                            ÖÖÖF
                            000F
                                     151
                                            COMPLETION CODES:
                            ÖÖÖF
                                     152
                            ÖÖÖF
                                     153
                                                    no such memory, or success
                            ÖÖÖF
                            000F
                                     155
                                            SIDE EFFECTS:
                            ÖÖÖF
                                     156
                                                    NONE
                            ÖÖÖF
                                     157
                            000F
                                     159
                            000F
                            ÖÖÖF
                                     160
                            ÖÖÖF
                     00F C
                            000F
                                          .Entry
                                                   VALIDATE_QUEUE, ^M<R2,R3,R4,R5,R6,R7>
                             0011
                                     163
                            0011
                       D0
                                     164
                                                    MOVL
                                                              TPA$L_NUMBER(AP),RO
                                                                                            ; Address to start at
0000000°EF
                                                              RO. ADDRESS
                                                                                            : Set "current"
                 50
                       DO
                            0015
                                     165
                                                    MOVL
                            0010
                                     166
                                     167
                            0010
                                                    MOVL
                                                                                            : Max number of links *** not used
                                                              VALIDATE_MAX,R1
                             0010
           5E
52
                                                    SUBL 2
                                                              #local_storage,SP
                                                                                             allocate storage from stack
                 5Ē
                       DŌ
                            001
                                                    MOVL
                                                              SP,R2
                                                                                             Allocate or stack
                                     171
                 56
                                                              R6
                       04
                                                    CLRL
                                                                                            : Counter
                                     172
173
                                                             RO,R3
(RO),HEADFL(R2),#8
           53
                 50
                       D<sub>0</sub>
                                                    MOVL
                                                                                            ; init last pointer
                                     174
175
                                                    TRYMEM
                                                                                            ; try memory at head of queue
       54
             08 A2
                       D0
                            0035
                                                              HEADFL(R2),R4
                                                    MOVL
                                                                                            ; next address
                                     176
177
                                                      Loop through flinks
                                     178
```

Page

(2)

```
179 20$:
                        0039
                                180
                                              TRYMEM
                                                        (R4), NEWFL(R2),#8
                                                                                     : try memory
                  E9
D1
13
                       0046
                                                       RO, rem err
newft. 32), headfl(R2)
                                181
                                              BLBC
                                                                                       Error
   08 A2
                                182
                        0049
                                              CMPL
                                                                                       Same as listhead?
                        004D
                                                        100$
                                              BEQL
                                                                                       Done with flinks
                                                       R6
R3, NEWBL (R2)
                  D6
                        004F
                                184
            563
542
542
                                              INCL
                                                                                       Increment counter
                  D1
12
   04 A2
                        0051
                                185
                                              CMPL
                                                                                       back link ok?
                                186
187
                                                       bad blink
R4,R3
                        0055
                                              BNEQ
                                                                                       branch if not
      53
54
                  DO
                       0057
                                              MOVL
                                                                                       save last pointer
                  D0
                       005A
                                188
                                              MOVL
                                                        NEWFL (R2), R4
                                                                                      move to next element
                  11
                                189
             DA
                       005D
                                              BRB
                                                                                     : Loop
                        005F
                                190
                        005F
                                191
                                                Search completed successfully, do final validation
                                192
193 100$:
                        005F
OC A2
                        005F
                                              ČMPL
                                                        NEWBL (R2), HEADBL (R2)
                                                                                     ; Same as listhead?
            2F
53
29
                                194
                                                        bad_blink
R3,READBL(R2)
                                                                                     ; Done with list
                        0064
                                              BNEQ
   OC A2
                  D1
                        0066
                                              CMPL
                                                                                     : does this check out?
                                196
197
                       006A
                                              BNEQ
                                                        bad_blink
                        006C
                                198
                        0060
                                                Queue is ok, check for empty queue
                        0060
                                199
            56
47
                                200 110$:
                                              TSTL
                  D5
12
                       0060
                                201
202
203
                       006E
                                              BNEQ
                                                        gueue_ok
                        0070
                                              PRINT
                                                        0,<The queue is empty> ;
                  31
          0046
                       007D
                                              BRW
                                                        VAL_Q_EXIT
                                204
                        0080
                                205 mem_err:
                        0080
            54
                                206
207
                  DD
                       0080
                                              PUSHL
                                              SIGNAL 1, NOTINPHYS
                        0082
                                                                                   ; Not in physical memory error
                                208
                       0094
                                210 bad_blink:
                       0095
            53
                  DD
                                              PUSHL
                        0097
                                              PRINT
                                213 < Error comparing backward link to previous structure address (!XL)>
                        0097
                                214
            56
54
                       00A4
                                              PUSHL
                                                       R6
                  DD
                       00A6
                                              PUSHL
                        8A00
                                              PRINT
                                217 <Error occured in queue element at address !XL, after tracing !UL element!%$> 218 brb val_q_exit 219 220 queue_ok:
                        8A00
                       0085
             0F
                   11
                        00B7
                        00B7
                                              PUSHL
             56
                  DD
                       00B7
                                                                                     ; Count
                                                        1, <Queue is complete, total of !UL element!%S in the queue>
                        00B9
                                              PRINT
                       0006
                                224 val_q_exit:
225 MOVI
226 RET
227
228 END ;
                        0006
       50
            01
                                                        #1,R0
                        0006
                                              MOVL
                       0009
                                              RET
                        00CA
                                                       VALIDATE
```

```
VALIDATE
                                                                                 16-SEP-1984 01:48:32
5-SEP-1984 03:34:48
                                    - Structure Validation module
                                                                                                         VAX/VMS Macro V04-00
                                                                                                                                         Page
                                                                                                                                               (2)
Symbol table
                                                                                                          [SDA.SRC]VALIDATE.MAR: 1
ADDRESS
                                    01
                 = 00000003
ARGS
BAD BLINK
                   00000095 R
                                    01
HEADBL
                 = 0000000C
HEADFL
                 = 00000008
LIB$SIGNAL
                                    01
                = 00000010
LOCAL STORAGE
MEM_ERR
                   00000080 R
MSG$_NOTINPHYS
                                    01
                 = 00000004
NEWB[
NEWFL
                 = 00000000
PRINT
QUEUE_OK
TPA$L_NUMBER
                                    Ŏi
                   000000B7 R
                 = 0000001C
TRYMEM
VALIDATE_MAX
                   00000000 R
                                    Ŏ1
                                    Ŏ1
VALIDATE QUEUE
                   0000000F RG
VAL_Q_EXIT
VAL_SET_MAX
                                    Ŏi
                   000000C6 R
                                    01
                   00000004 RG
                                                       Psect synopsis!
PSECT name
                                    Allocation
                                                          PSECT No.
                                                                      Attributes
. ABS
                                    00000000
                                                          00
                                                                0.)
                                                                      NOPIC
                                                                               USR
                                                                                      CON
                                                                                            ABS
                                                                                                   LCL NOSHR NOEXE NORD
                                                                                                                           NOWRT NOVEC BYTE
                                                          01 (
02 (
03 (
BLANK .
                                                  202.)
                                    00000CA
                                                                1.)
                                                                      NOPIC
                                                                               USR
                                                                                      CON
                                                                                            REL
                                                                                                   LCL NOSHR
                                                                                                                EXE
                                                                                                                       RD
                                                                                                                              WRT NOVEC BYTE
                                                                2.)
3.)
                                    0000000
                                                                      NOPIC
                                                                                                                              WRT NOVEC BYTE
                                                                               USR
                                                                                      CON
                                                                                            ABS
                                                                                                   LCL NOSHR
                                                                                                                EXE
                                                                                                                       RD
LITERALS
                                    000000F5
                                                                      NOPIC
                                                                               USR
                                                                                      CON
                                                                                            REL
                                                                                                   LCL NOSHR
                                                                                                                EXE
                                                                                                                       RD
                                                                                                                           NOWRT NOVEC BYTE
                                                    Performance indicators!
                                                                         le
```

LIB VO4

| Phase | Page faults | CPU Time | Elapsed Time |
|--|-------------|----------------------------|----------------------------|
| laitialiantian | 20 | 00.00.00 | 00.00.00 00 |
| Initialization Command processing | 29 111 | 00:00:00.05 00:00:00.48 | 00:00:00.80 00:00:02.85 |
| Pass 1 | 163 | 00:00:01.68 | 00:00:06.93 |
| Symbol table sort | Ō | 00:00:00.12 | 00:00:00.12 |
| Pass 2 | 53 | 00:00:00.47 | 00:00:02.15 |
| Symbol table output | 3 | 00:00:00.02 | 00:00:00.02 |
| Psect synopsis output | Ž | 00:00:00.02 | 00:00:00.02 |
| Cross-référence output Assembler run totals | 363 | 00:00:00.00 00:00:02.84 | 00:00:00.00 00:00:12.89 |

The working set limit was 1050 pages.
13151 bytes (26 pages) of virtual memory were used to buffer the intermediate code.
There were 10 pages of symbol table space allocated to hold 182 non-local and 8 local symbols.
228 source lines were read in Pass 1, producing 21 object records in Pass 2.
13 pages of virtual memory were used to define 12 macros.

VALIDATE VAX-11 Macro Run Statistics 16-SEP-1984 01:48:32 VAX/VMS Macro V04-00 5-SEP-1984 03:34:48 [SDA.SRC]VALIDATE.MAR;1 L18 V04 - Structure Validation module Page (2) Macro library statistics ! Macro library name Macros defined _\$255\$DUA28:[SDA.OBJ]SDALIB.MLB;1 _\$255\$DUA28:[SYS.OBJ]LIB.MLB;1 _\$255\$DUA28:[SYSLIB]STARLET.MLB;2 TOTALS (all libraries) 9 275 GETS were required to define 9 macros. There were no errors, warnings or information messages. MACRO/LIS=LIS\$: VALIDATE/OBJ=OBJ\$: VALIDATE MSRC\$: VALIDATE/UPDATE=(ENH\$: VALIDATE) + EXECML\$/LIB+LIB\$: SDALIB/LIB

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